

FIG. 2

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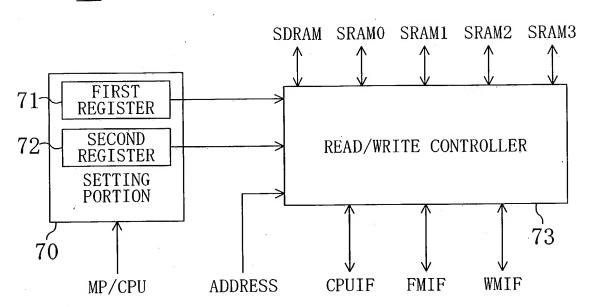


FIG. 3

operating MPEG-4 process:	MPEG-4 processing (MP)	video display (VIF, GFX) processing	CPU processing
A	8Mbit		
В	6Mbit	2Mbit	
S	4Mbit	4Mbit	-
D	4Mbit	2Mbit	2Mbit
田		4Mbit	4Mbit
佦		8Mbit	
Ð			8Mbit

FIG. 44

memory	IOS	SDRAM	SR	SRAM3	SRI	SRAM2	SR	SRAMI	SR	SRAMO
first register bit9 bit7 bit6 bit5 bit4 bit3 bit2 bit1 bit0	bit9	bit8	bit7	bit6	bit5	bit4	bit3	bit2	bitl	bit0
initial value	0	0	0	0	0	0	0	0)	0(

FIG. 4B

		_		
meaning	work region	frame region	CPU region	pesnun
bit0	0	1	0	1
bit1	0	0	1	1

FIG. 5A

memory	SDRAM	SRAM3	SRAM2	SRAM1	SRAM0
second register	bit5-4	bit3	bit2	bitl	bit0
initial value	00	0	0	0	0

FIG. 5B

bit0	meaning
0	2Mbit
1	4Mbit

FIG. 5C

bit5	bit4	meaning
0	0	unused
0	1	64Mbit
1	0	128Mbit
1	1	256Mbit

				\$0040000	0000000\$	· .		CPU memory map
FIG. 6B		frame region		CPU region	CPU region	work region	work region	map
**************************************	11111010		\$0100000	0000000\$	\$0080000	\$0040000	0000000\$	MP memory map
		·		·				y map
				\$0000000	\$0080000			CPU memory map
FIG. 6A		frame region	·	CPU region	CPU region	work region	work region	MP memory map

